IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ın Ke	Patent Application of:)	
	Min-hwa Chi) Examiner:	Not yet assigned
Applie	cation No. Not yet assigned) Art Unit:	Not yet assigned
Filed:	Concurrently herewith)) \	
For:	TRANSISTOR AND LOGIC CIRCUIT ON THIN SILICON-ON-INSULATOR WAFERS BASED ON GATE INDUCED DRAIN LEAKAGE CURRENTS))))))	

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with a copy of the document listed on that form. It is respectfully requested that the cited document be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

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Respectfully submitted, 206-292-8600h
Dated: 25/02 Dated: 25/02 Jan Carol Little Reg. No. 41,181
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Substitute for F rm 1449A/PTO (Modified) INFORMATION DISCLOSURE STATEMENT BY APPLICANT Complete if Known Application Number Filing Date Herewith First Named Inventor: Min-hwa Ch

Application Number Not yet assigned

Filing Date Herewith

First Named Inventor: Min-hwa Chi

Group Art Unit Not yet assigned

Date Considered

(use as many sheets as necessary) **Examiner Name** Not yet assigned **Sheet** of Attorney Docket Number 004192.P053D2 **U.S. PATENT DOCUMENTS** Cite Exam. U.S. Patent Document Name of Patentee or Applicant Date of Pages, Columns, Lines, Initial* No.1 of Cited Document Publication of Where Relevant Number Kind Code² (If known) Cited Document Passages or Relevant MM-DD-YYYY Figures Appear 5,448,513 Hu et al. 09/05/1995

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Exam. Initial*	Cite No. ¹	Forei	gn Patent D Number ⁴	Nocument Kind Code ⁵ (If known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵

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